PATENT USSN: 10/820,852

Atty Dkt: 033773M068

## **AMENDMENT**

## IN THE CLAIMS:

Please amend the claims as follows:

1. (Original) A flip chip bonder comprising substrate holding means arranged in a chip die bonding area and a chip die bonder for bonding a semiconductor chip having a plurality of electrodes projecting from its front surface to a substrate to be implemented, held on the substrate holding means, wherein

the flip chip bonder comprises a chuck table for holding a semiconductor chip, which can be moved to a semiconductor chip take-in area, a semiconductor chip take-out area and an electrode cutting area, a cutting means having a cutting tool for cutting the plurality of electrodes projecting from the front surface of the semiconductor chip held on the chuck table and arranged in the electrode cutting area to make them uniform in height, a semiconductor chip take-in means for carrying a semiconductor chip before processing to the chuck table positioned in the semiconductor chip take-in area, and a semiconductor chip conveying means for carrying a semiconductor chip after processing, which is held on the chuck table positioned in the semiconductor chip take-out area, to the chip die bonder.

- 2. (Original) The flip chip bonder according to claim 1 which comprises a processing fluid supply means for supplying a processing fluid toward the semiconductor chip held on the chuck table in the electrode cutting area.
- 3. (Original) The flip chip bonder according to claim 2, wherein the processing fluid supply means supplies ionized air.
- 4. (New) The flip chip bonder according to claim 1, wherein the cutting tool is a mechanical tool.

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5. (New) A flip chip bonder comprising substrate holding means arranged in a chip die bonding area and a chip die bonder for bonding a semiconductor chip having a plurality of electrodes projecting from its front surface to a substrate to be implemented, held on the substrate holding means, wherein

the flip chip bonder comprises a chuck table for holding a semiconductor chip, which can be moved to a semiconductor chip take-in area, a semiconductor chip take-out area and an electrode cutting area, a mechanical cutting means having a cutting tool for cutting the plurality of electrodes projecting from the front surface of the semiconductor chip held on the chuck table and arranged in the electrode cutting area to make them uniform in height, a semiconductor chip take-in means for carrying a semiconductor chip before processing to the chuck table positioned in the semiconductor chip take-in area, and a semiconductor chip conveying means for carrying a semiconductor chip after processing, which is held on the chuck table positioned in the semiconductor chip take-out area, to the chip die bonder.